EXPERIMENT PE1: POWER DEVICE CONTROL

Related course: KIE3008 (Power Electronics) or KEEE4265 (Power Electronics)

OBJECTIVES:

- 1. To arrange and test rectifier circuits that contain power devices
- 2. To record the output of power device circuits
- 3. To determine the capability and use of power devices

EQUIPMENT:

15V DC power supply, Voltage control unit, 6-pulse control unit, Thyristor (panel), Diode (panel), Current probe, Digital oscilloscope, Analog multimeter, 3-phase converters, Isolation amplifier, Inductive load, Lamp

INSTRUCTIONS:

- 1. Record all your results and observations in a log book / paper
- 2. Follow the experiment procedure properly

REFERENCE(S):

Refer to the main references of KIE3008 or KEEE4265

TESTS:

Test 1: Controlled one-pulse half-wave rectifier

- Test 2: Balanced two-pulse circuit
- Test 3: Fully-controlled AC power controller with thyristor in non-parallel circuit

INTRODUCTION:

For control of electric power or power conditioning, conversion of electric power from one form to another is necessary. Static power converters perform power conversion. A converter may be considered a switching matrix. A controlled rectifier is a type of converter that changes sinusoidal waveforms into DC form of controllable output. Thus, a converter (building block) is a basic module of a power electronics system. To be more specific, in AC to DC and DC to AC conversions, rectifier refers to converter when the average power flow is from AC to DC side. For a given AC input voltage, the magnitude of average output voltage in thyristor converters can be controlled by delaying the instants at which thyristors are allowed to start conduction.

TEST 1: Controlled one-pulse half-wave rectifier

Objective: To investigate the relationship between load power, firing angle α° and current flow angle θ°

- 1. Connect the circuit as shown in Figure 1. Use firing angle = 90° and save the following oscilloscope figures in a pendrive or capture using a camera:
 - a) Input voltage V_{in}
 - b) Voltage across the load V_{load}
 - c) Current through the load I_d
 - d) Voltage across the thyristor V_{AK}
 - Then, repeat step 1 for firing angle = 135° .
- 2. Change the firing angle using voltage control from 0V to 10V at interval of 1V. For each step, measure and record the power of the load. Using oscilloscope, measure and record the current flow angle. Complete Table 1.

- a) Input voltage Vin
- b) Voltage across the load V_{load}
- c) Current through the load I_d
- d) Voltage across the thyristor V_{AK}

Then, compare the figures obtained between step 1 and 3. Give comment.



Figure 1

| I able 1 |
|----------|
|----------|

| Voltage control (V) | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 |
|------------------------|---|---|---|---|---|---|---|---|---|----|
| Firing angle | | | | | | | | | | |
| Power (W) | | | | | | | | | | |
| Current flow angle | | | | | | | | | | |

QUESTIONS:

From Table 1, plot a graph of power against firing angle and a graph of current flow angle versus firing angle. Comment on the graph.

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TEST 2: Balanced two-pulse circuit

Objective: To study the control properties for balanced two pulse rectifier and its response against resistive load and compound loads

- 1. Connect the circuit as shown in Figure 2. Change the firing angle using voltage control from 0V to 10V at interval of 1V. Measure and record the DC voltage at load $V_{d\alpha}$ in Table 2. At firing angle of 90°, save the input voltage V_{in} and load voltage V_{load} curves from the oscilloscope figure in a pendrive or capture using a camera.
- 2. Insert a diode between point *x* and *y*, where the cathode of the diode faces *y*. At firing angle of 90°, save the input voltage V_{in} and load voltage V_{load} curves from the oscilloscope figure in a pendrive or capture using a camera.





| Table 2 | | | | | | | | | | |
|----------------------------|---|---|---|---|---|---|---|---|---|----|
| Voltage control (V) | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 |
| DC voltage at load | | | | | | | | | | |
| (<i>V</i> _{dα}) | | | | | | | | | | |

TEST 3: Fully-controlled AC power controller with thyristor in non-parallel circuit

Objective: To understand the control properties of AC power controller using two thyristors with compound loads and its behavior

- 1. Connect the circuit as in Figure 3. Vary the firing angle using voltage control from 0V to 10 V. At each step, measure and record load voltage V_{α} . Complete Table 3.
- 2. Change the firing angle to 90°. Save the following oscilloscope figures in a pendrive or capture using a camera:
 - a) Input voltage V_{in}

b) Voltage across the load V_{load}

- c) Current through the load I_d d) Voltage across the thyristor V_{AK}
- $V_{AK} = (V_A) (V_K)$ Three-phase 10A fuse $V_{load} = (V_L +) - (V_L -)$ transformer R $V_{in} = (V_R) - (N)$ Lamp Thyristor socket Multimeter 11 Inductive load Digital oscilloscope Control 6-pulse control unit voltage unit CH1 CH2 Û VL-**Current probe** R 8 $\circ \circ [$ Isolation amplifier
 - Figure 3

| Table 3 | | | | | | | | | | |
|-------------------------------------|---|---|---|---|---|---|---|---|---|----|
| Voltage control (V) | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 |
| AC voltage at load (V_{α}) | | | | | | | | | | |

QUESTIONS:

- 1. From Table 2, plot a graph of (V_{da}/V_{d0}) versus firing angle. (Note: V_{d0} is the voltage value at load at firing angle of 0°). Comment on the graph.
- 2. From Table 3, plot a graph of (V_{a}/V_{a0}) versus firing angle. Comment on the graph.

END OF EXPERIMENT

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